

FIG. 1
(PRIOR ART)

TITLE: CIRCUIT SYNCHRONIZATION APPARATUS AND METHOD
INVENTORS NAME: Brian N. Poindexter

2/9

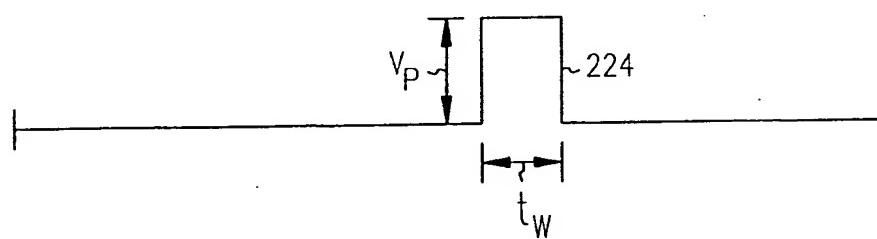
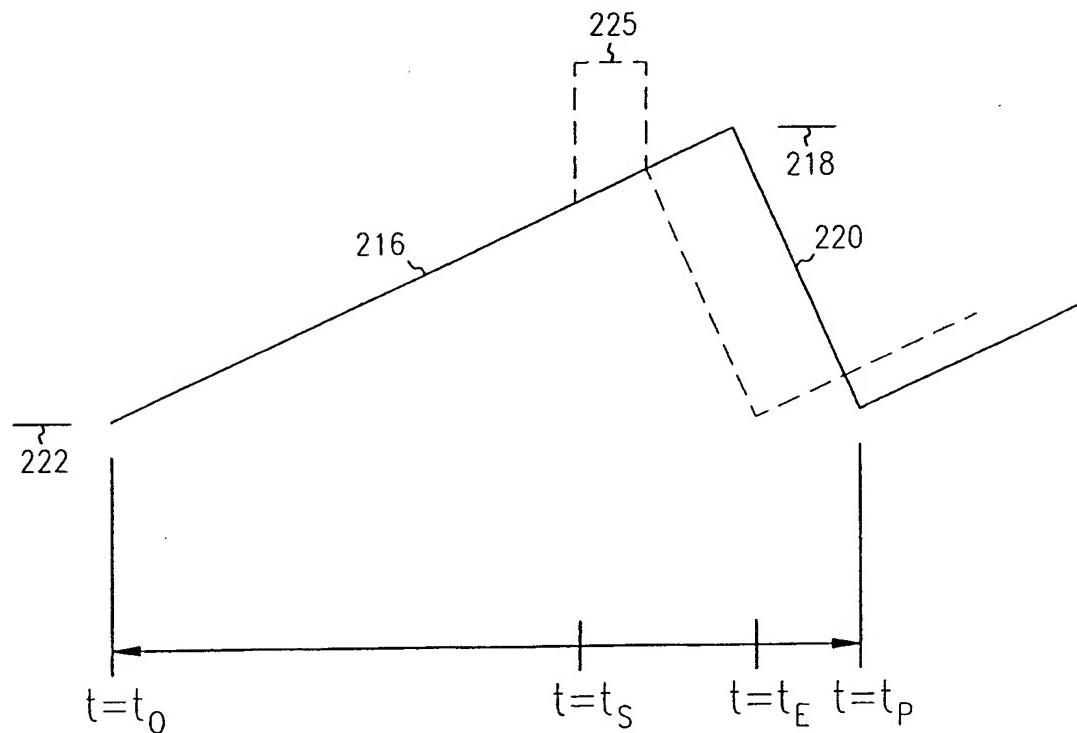


FIG. 2
(PRIOR ART)

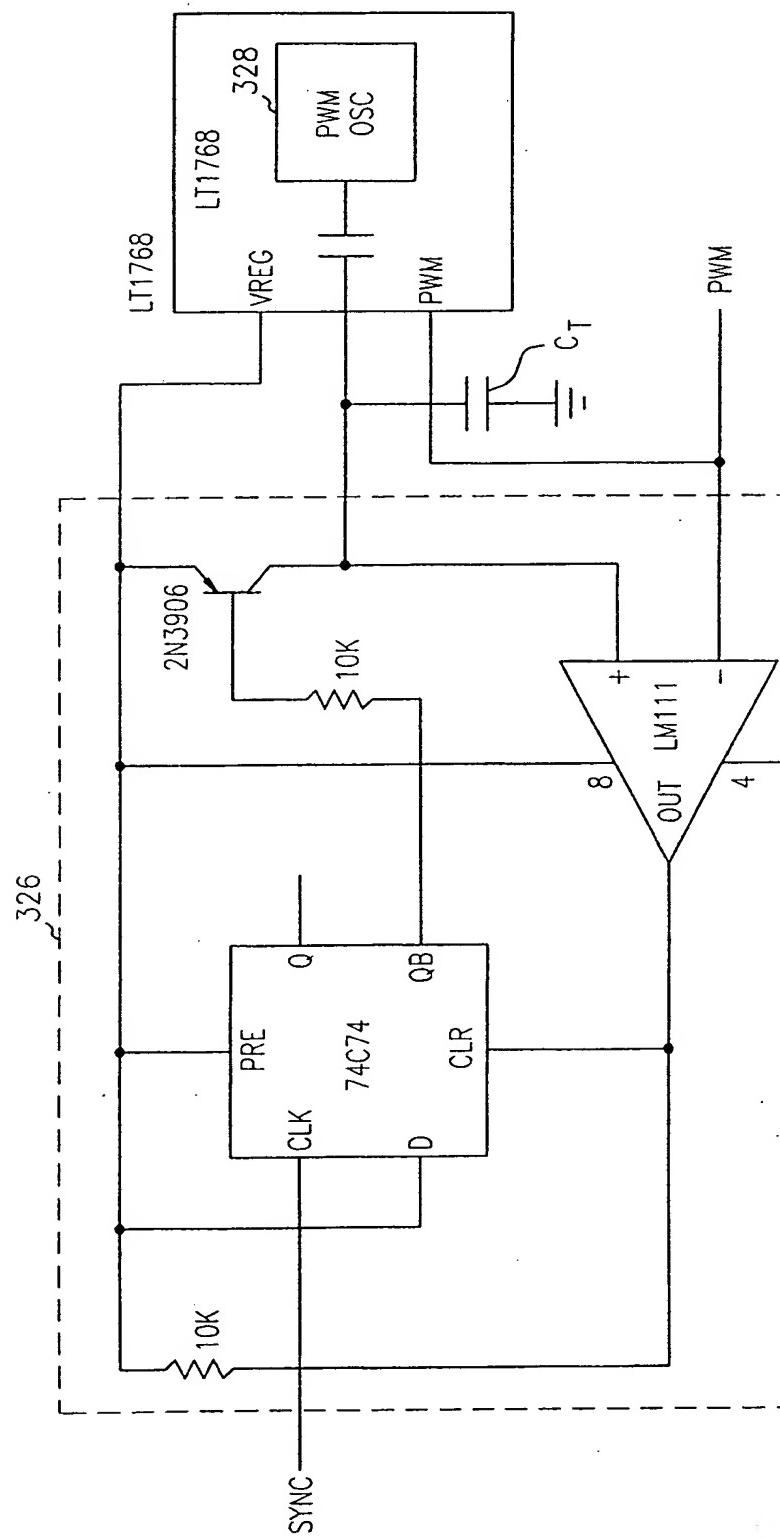


FIG. 3
(PRIOR ART)

TITLE: CIRCUIT SYNCHRONIZATION APPARATUS AND METHOD
INVENTORS NAME: Brian N. Poindexter

4/9

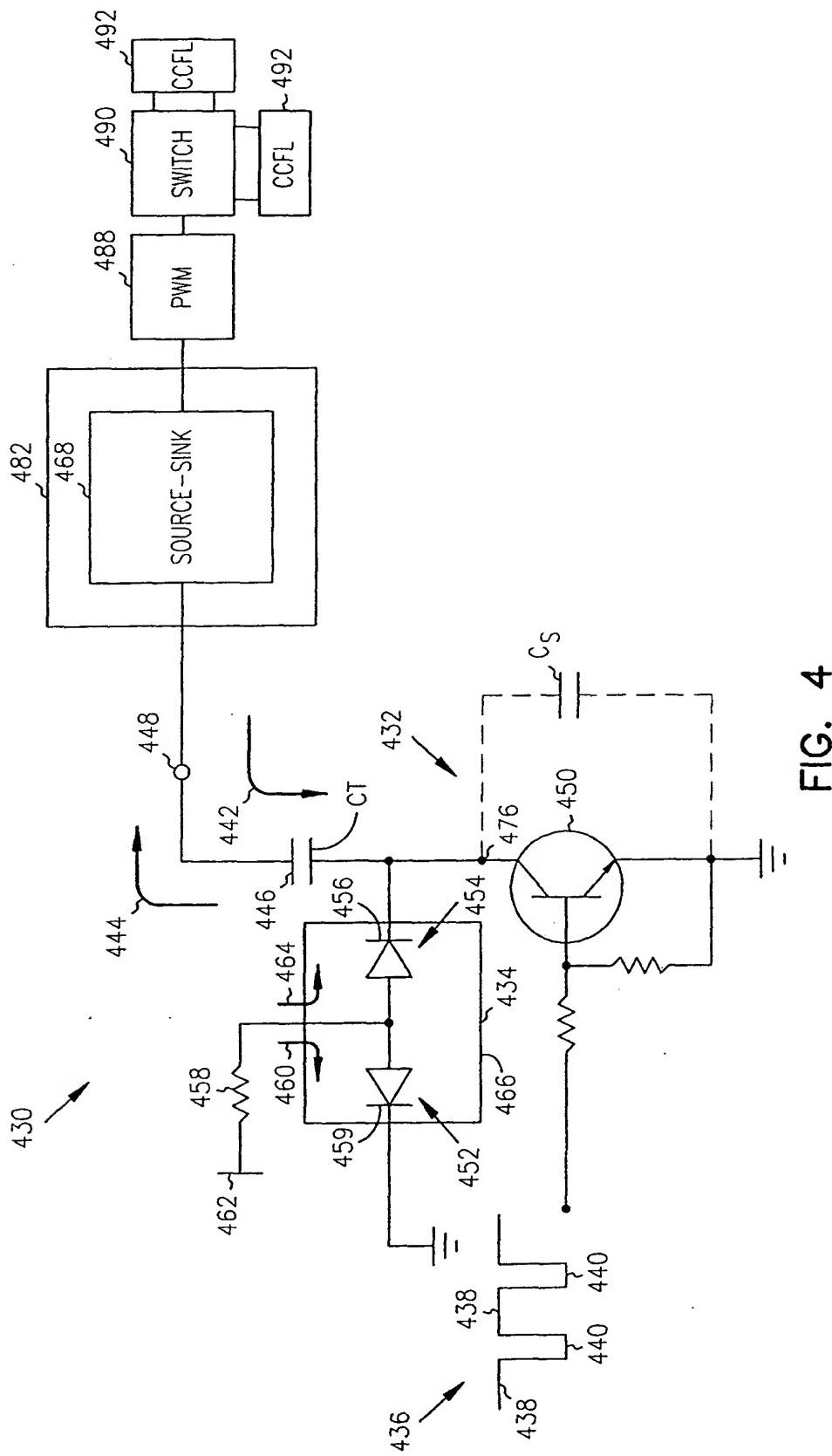


FIG. 4

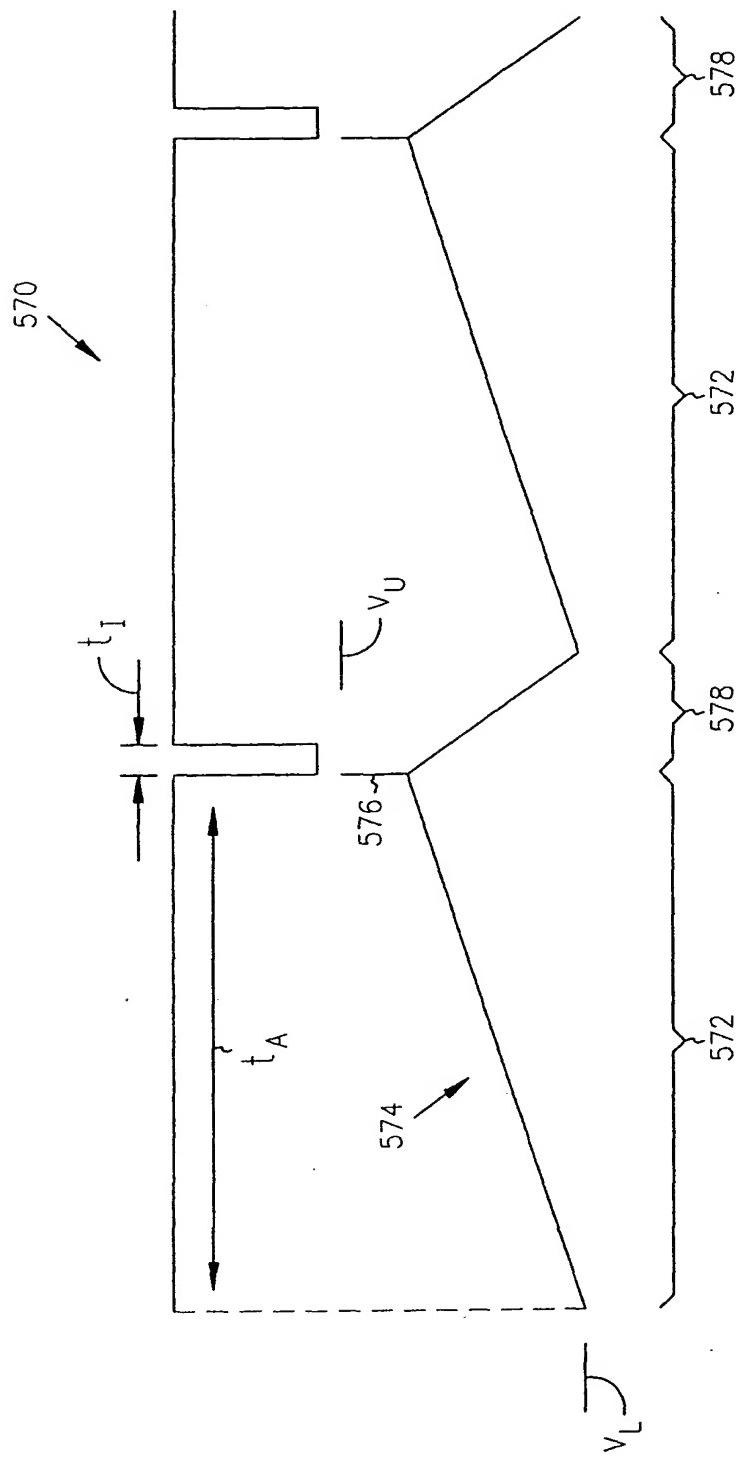


FIG. 5

TITLE: CIRCUIT SYNCHRONIZATION APPARATUS AND METHOD
INVENTORS NAME: Brian N. Poindexter

6/9

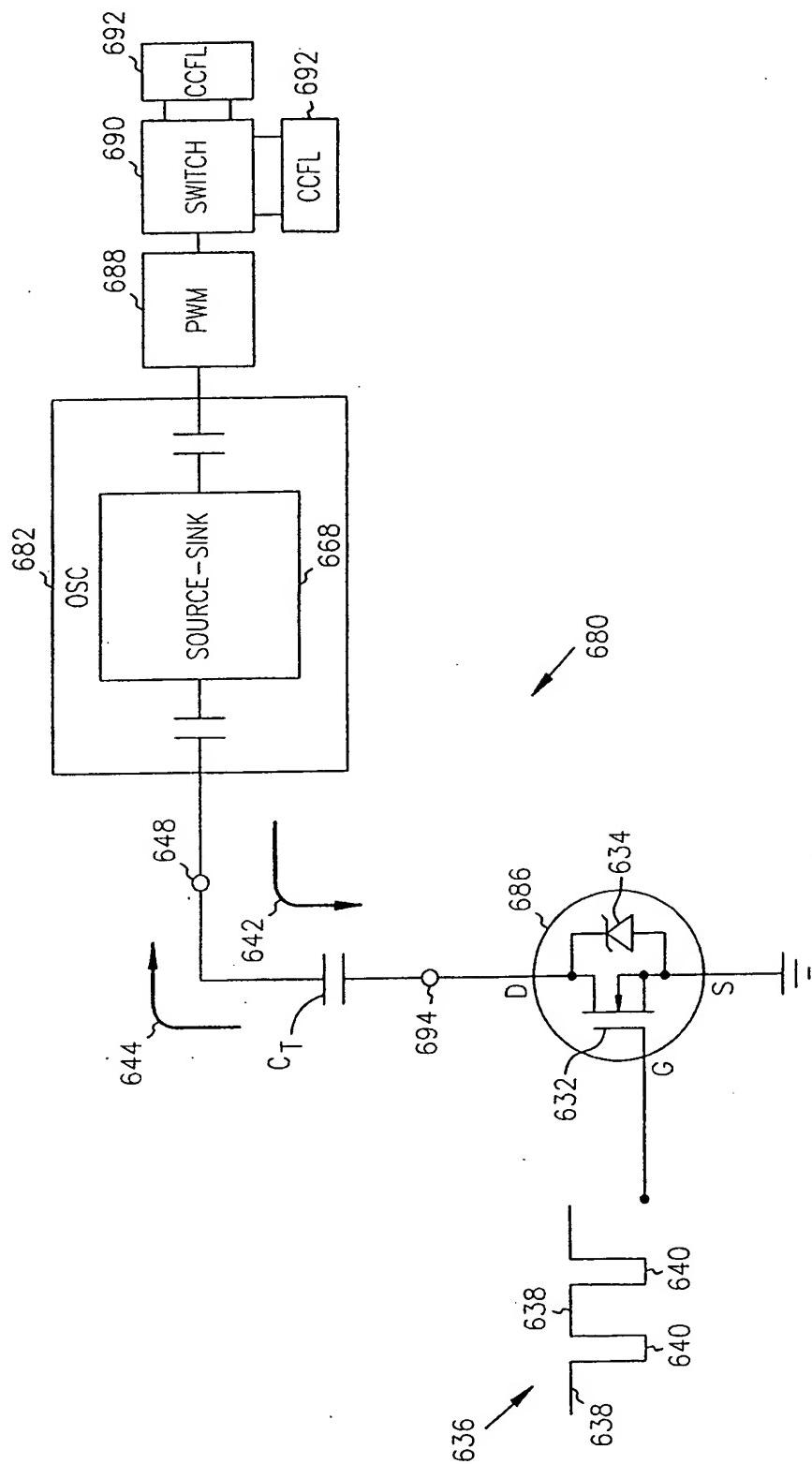


FIG. 6

TITLE: CIRCUIT SYNCHRONIZATION APPARATUS AND METHOD
INVENTORS NAME: Brian N. Poindexter

7/9

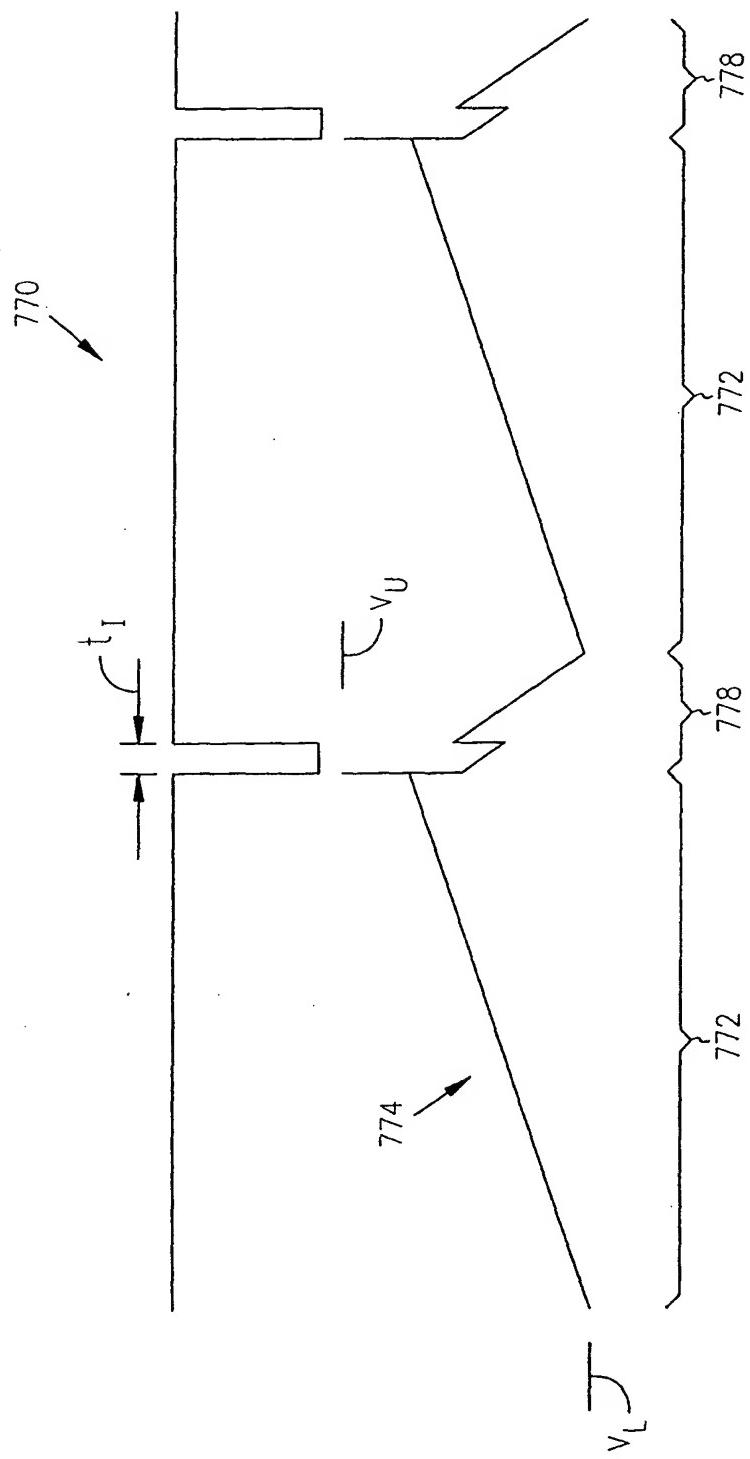


FIG. 7

TITLE: CIRCUIT SYNCHRONIZATION APPARATUS AND METHOD
INVENTORS NAME: Brian N. Poindexter

8/9

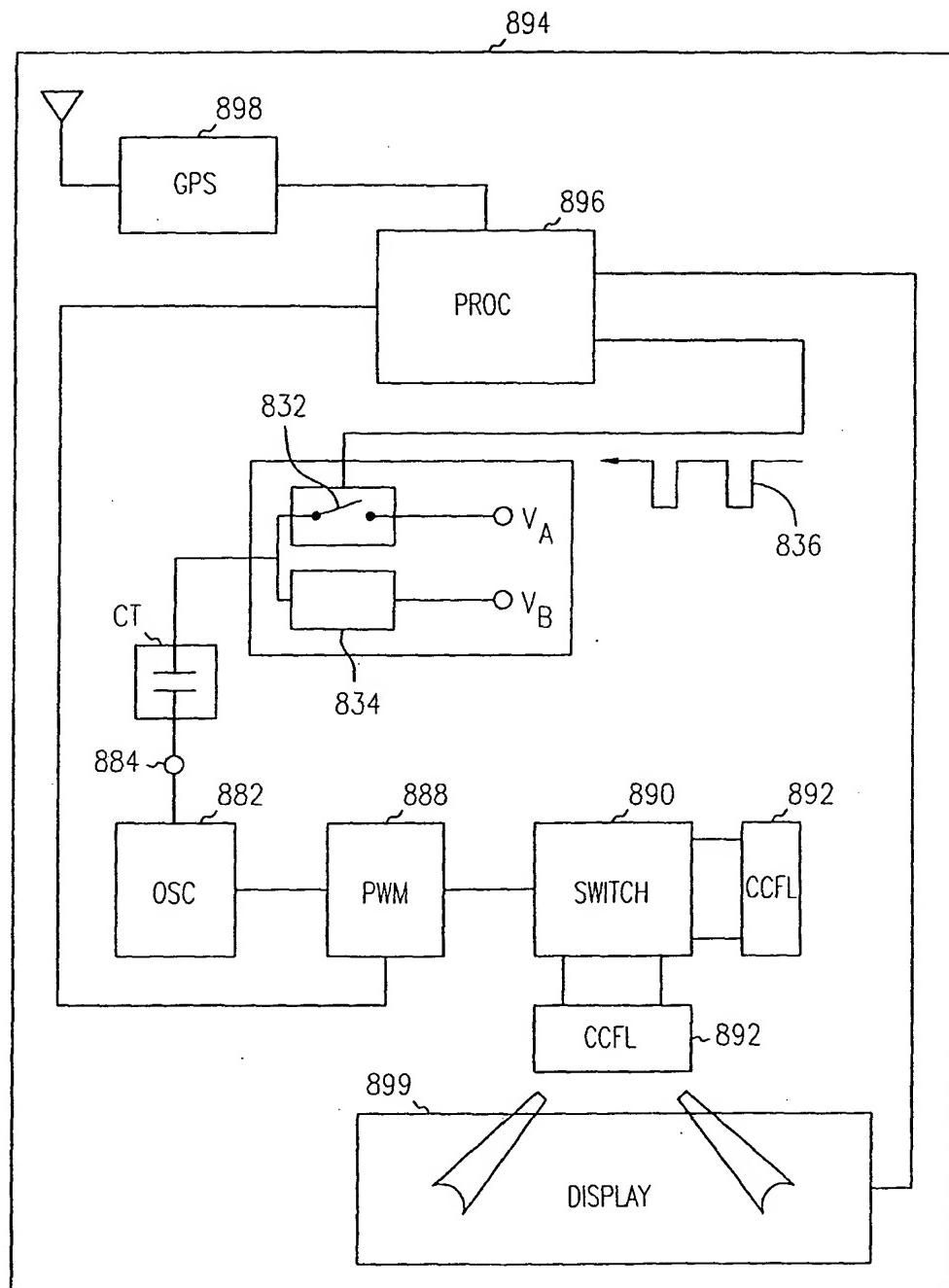


FIG. 8

9/9

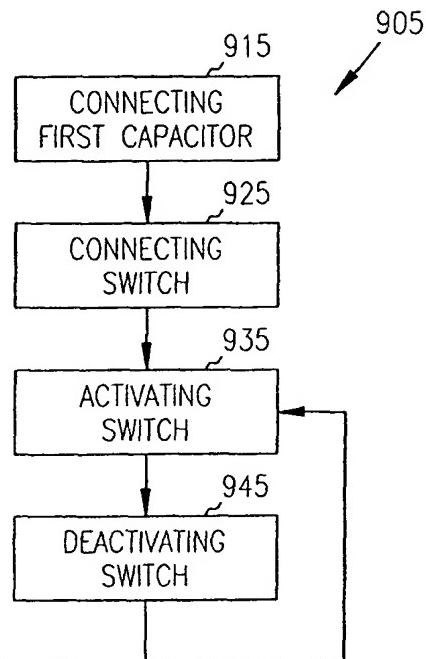


FIG. 9

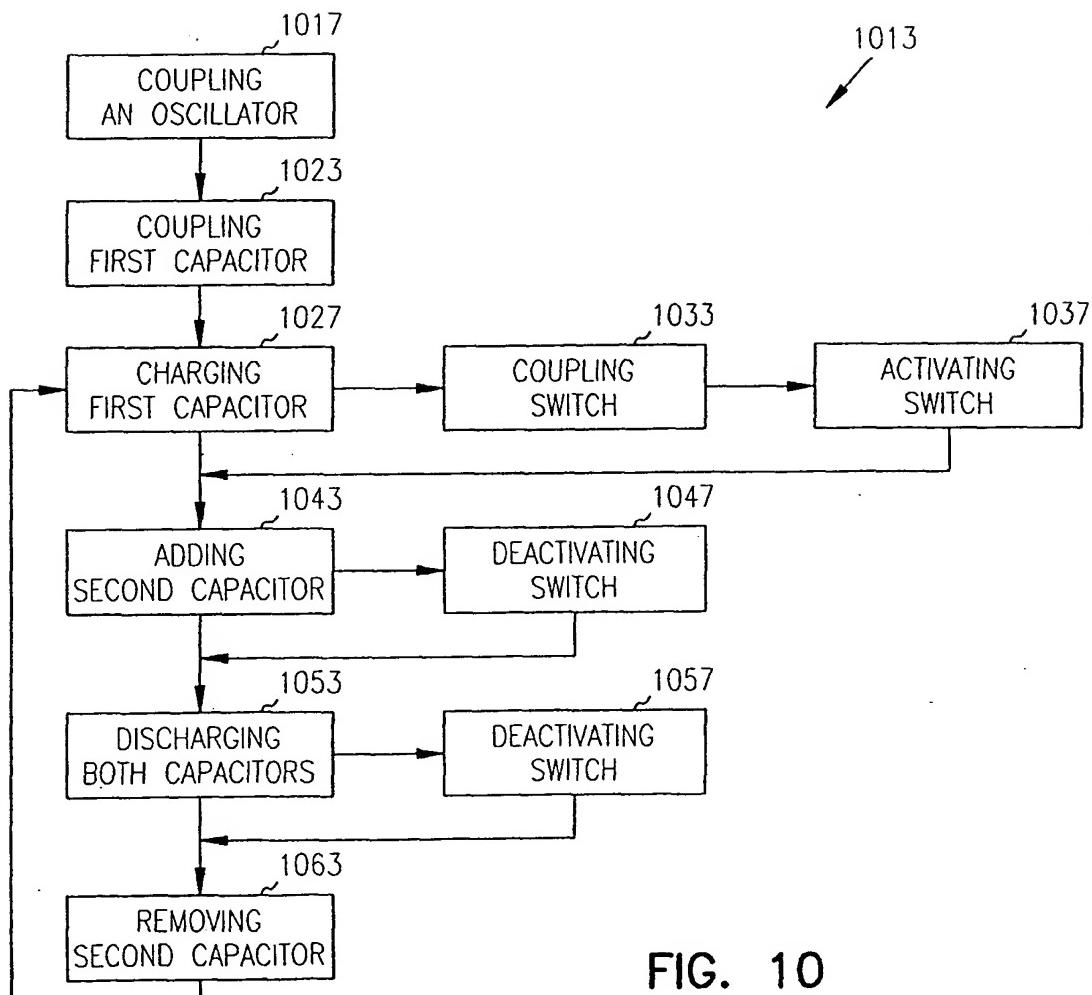


FIG. 10